

## **ELECTRONICS & COMMUNICATION ENGINEERING**

## RESEARCH CENTRE

## **Supervisor Details**

S1.	No.	Name	Designation	Registration ID	
	1	Dr. Vijayashree R Budyal	Professor	VTU0817110	

## List of Internal / External Research Scholars & Status

Sl. No.	USN	Name of the Scholar	Year of Registration	Title	Internal/ External	Status
1.	1VE18PEC01	Mrs. Kursheed	2018	A NOVEL FRAMEWORK SOLUTION TO OPTIMIZE MULTIPLE ACCESSING SYSTEM TO	Internal	Comprehensive viva is
				IMPROVE THE PERFORMANCE OF 5G COMMUNICATIONS USING COGNITIVE RADIO		completed
				NETWORK		
2.	1VE16PEJ026	Mrs. Hridya	2016	DESIGN AND DEVELOPMENT OF INTELLIGENT SECURITY ALGORITHMS FOR VANETS	External	Course work is completed
3.	1VE17PEA07	Mrs. Swapna Ch	2017	DESIGN AND DEVELOPMENT OF ENERGY EFFICIENT ALGORITHMS FOR DATA GATHERING, AGGREGATION, ROUTING AND DISSEMINATION IN WIRELESS SENSOR NETWORKS USING ARTIFICIAL INTELLIGENCE	Internal	Comprehensive viva is completed

E&CE Research Centre Head HEAD, DEPT. OF E & CE SRI VENKATESHWARA COLLEGE OF ENGINEERING BANGALORE-562 157